



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. 98,485-E)

PATENT

GP/1762
#3
BT
03.25-0

In the Application of:

Dean Jean

Serial No.: 10/036,704

Filing Date: December 21, 2001

For: Edge Connectors for Printed Circuit
Boards Comprising Conductive Ink

Examiner: B. Talbot

Group Art Unit: 1762

TRANSMITTAL LETTER

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In regard to the above identified application,

1. We are transmitting herewith the attached:

- a) Information Disclosure Statement;
- b) Form PTO-1449 with 26 cited references; and
- b) Postcard.

2. With respect to fees:

- a) No fees are necessary at this time.
- b) Please charge any underpayment or credit any overpayment our Deposit Account, No. 13-2490.

3. CERTIFICATE OF MAILING UNDER 37 CFR § 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described in paragraph 1, are being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on the 19th day of February 2002.

Respectfully submitted,

Steven J. Sarussi
Registration No. 32,784

RECEIVED
MAR 11 2002
TC 1700



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. 98,485-E)

In the Application of:)
)
 Denis Jean) Before the
) Examiner: B. Talbot
 Serial No. 10/036,704)
)
 Filed: December 21, 2001) Art Unit: 1762
)
 For: Edge Connectors For Printed Circuit)
 Boards Comprising Conductive Ink)

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

This prior art statement is filed under 37 C.F.R. §§1.97-1.98 in compliance with the duty of disclosure set forth in 37 C.F.R. §1.56.

In the judgment of the undersigned, the references listed on the attached Form PTO-1449 may be material to the Examiner's consideration of the presently pending claims. However, the references have not been reviewed in sufficient detail to make any other representation and, in particular, no representation is intended as to the relative relevance between references, whether cited in this statement or prior statements. This statement is not a representation that the listed references have effective dates early enough to be "prior art" within the meaning of 35 U.S.C. §102.

1. U.S. Patent 4,045,636, issued August 30, 1977.

McDonnell Boehnen Hulbert & Berghoff
300 S. Wacker Drive
Chicago, Illinois 60606
(312) 913-0001

RECEIVED
MAR 11 2002
TC 1700

2. U.S. Patent No. 4,243,455, issued January 6, 1981.
3. U.S. Patent No. 4,517,739, issued May 21, 1985.
4. U.S. Patent No. 4,896,250, issued January 23, 1990.
5. U.S. Patent No. 4,964,948, issued October 23, 1990.
6. U.S. Patent No. 5,042,971, issued August 27, 1991.
7. U.S. Patent No. 5,100,695, issued March 31, 1992.
8. U.S. Patent No. 5,380,271, issued January 10, 1995.
9. U.S. Patent No. 5,599,595, issued February 4, 1997.
10. U.S. Patent No. 5,688,146, issued November 18, 1997.
11. U.S. Patent No. 5,716,663, issued February 10, 1998.
12. U.S. Patent No. 4,545,926, issued October 8, 1985.
13. U.S. Patent No. 5,484,648, issued January 16, 1996.
14. U.S. Patent No. 5,928,547, issued July 27, 1999.
15. U.S. Patent No. 5,250,228, issued October 5, 1993.
16. U.S. Patent No. 4,917,466, issued April 17, 1990.
17. U.S. Patent No. 4,578,215, issued March 25, 1986.
18. U.S. Patent No. 5,049,313, issued September 17, 1991.
19. U.S. Patent No. 5,057,245, issued October 15, 1991.
20. U.S. Patent No. 6,265,051, issued July 24, 2001.
21. EP 0 538 978 A2, published April 28, 1993.
22. Wall, C.I., "Polymer Thick Films: What Are They, How Are They Used and Where?", *Circuit World*, Vol. 12, No. 2, pages 1-4, (1986).

23. "Electra Thick Film Profile. PTF Polymer Thick Film. What Is It; An How Is It Used?" *ELECTRA POLYMERS & CHEMICALS, LTD.*, (1984/85) pp. 1-9.
24. Technical Data Sheet ED7000 Series, "Carbon Resistor Pates For Rigid Circuit Boards:", *Electra Polymers & Chemicals, Ltd.*, ED7000-09/97rev5, pp. 1-2.
25. "Goldstone #3000 Conductive Overprint Ink", *Methode Development Co.*, #840330, (1984).
26. "Low cost edge tabs for circuit cards and hybrid substrates", Research Disclosure., no. 302, June 1989 (1989-06), page 441 XP000034774 INDUSTRIAL OPPORTUNITIES LTD. Havant., GB ISSN: 0374-4353.

In accordance with MPEP Sections 609 and 707.05(b), it is requested the document cited (including any cited in applicant's specification which is not repeated on the attached Form PTO-1449) be given thorough consideration and that it be cited of record in the prosecution history of the present application by initialing on Form PTO-1449. Such initialing is requested even if the Examiner does not consider a cited document to be sufficiently pertinent to use in a rejection, or otherwise does not consider it to be prior art for any reason, or even if the Examiner does not believe that the guidelines for citation have been fully complied with. This is requested so that each document becomes listed on the face of the patent issuing on the present application.

Respectfully submitted,
McDONNELL BOEHNEN
HULBERT & BERGHOFF
300 South Wacker Drive
Chicago, Illinois 60606
(312) 913-0001

Dated: February 19, 2002

By: _____

Steven J. Sarussi
Reg. No. 32,784

McDonnell Boehnen Hulbert & Berghoff
300 S. Wacker Drive
Chicago, Illinois 60606
(312) 913-0001